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| intermediate representation thread hierarchica | Search |

Scholar Results **1 - 10** of about **32** for <u>intermediate</u> <u>representation</u> <u>thread</u> <u>hierarchical</u> <u>web</u> <u>heterogeneou</u>

ESys. NET: A New Solution for Embedded Systems Modeling and Simulation
J Lapalme, EM Aboulhamid, G Nicolescu, L Charest, ... - ACM SIGPLAN/SIGBED 2004 Conference on Languages, Compilers, ... - portal.acm.org
... be used as a standard **intermediate** format for ... pointers, reflective capabilities, remoting, dynamic **thread** creating/control ... (a) High level **system representation** ...
Cited by 2 - Web Search - portal.acm.org

Distributed Java Virtual Machine with **Thread** Migration
W Zhu - cs.hku.hk
... in DJVM that can provide a single **system** image (SSI ... We provide in- depth study on **thread** migration, as well as ... object sharing in DJVM based on Java **memory** model ...
View as HTML - Web Search - cs.hku.hk

[PS] ASSET-A utomated S ynthesi S of E mbedded Sys T ems
S Jain, V Sinha - cse.iitd.ernet.in
... parallel processes and has an **intermediate representation**(ESIR) based ... a single Finite State Machine- like **representation**. ... there is a single **thread** of execution ...
View as HTML - Web Search

[PS] A Framework for Development of Embedded Systems
S Jain, V Sinha - cse.iitd.ernet.in
... parallel processes and has an **intermediate representation**(ESIR) based ... a single Finite State Machine- like **representation**. ... there is a single **thread** of execution ...
View as HTML - Web Search - cse.iitd.ernet.in

A Framework for Opportunistic Cluster Computing Using JavaSpaces
J Batheja, M Parashar - HPCN Europe, 2001 - caip.rutgers.edu
... in terms of instruction sets, process **representation** and ... include cached input and **intermediate** file data. ... for work stealing, **thread management**, and marshalling ...
Cited by 1 - View as HTML - Web Search - caip.rutgers.edu - ece.rutgers.edu - portal.acm.org

Technischer Bericht
X Fu, C Kappler, H Tschofenig, T Berichte - ifi.informatik.uni-goettingen.de
... whereas it is possible that some **intermediate** CASP nodes ... traditional way we can build a **hierarchical** of protocols. ... 32 ~ 128 bytes plus a **thread memory** page (4k ...
View as HTML - Web Search - ifi.informatik.uni-goettingen.de

Designing the B-1
MJ Carey, JM Hellerstein, M Stonebraker - db.cs.berkeley.edu
... need for independence of flexible **hierarchical** and networked ... in particular, device drivers, lightweight **thread management**, and network ... **representation** should be ...
View as HTML - Web Search - cs.berkeley.edu

An Expressive Language and Efficient Execution **System** for Software Agents
G Barish, CA Knoblock - Journal of Artificial Intelligence Research, 2005 - cs.cmu.edu
... of both I/O stalls as well as **thread** context-switching ... 4.1 Plan **Representation** In our language, plans are textual ... on that data (and the **intermediate** results it ...
View as HTML - Web Search - cs.cmu.edu - isi.edu

Rendering and visualization in affordable parallel environments

D Bartz, C Silvia, BO Schneider - cdserv4.inria.fr

... scheduled by the **thread** library (user **thread** scheduling ... information is selected from the different **web** sites of ... who buys mem- ory from the **system** vendor anyway ...

Cited by 2 - View as HTML - Web Search

[PS] A Pattern Language for Parallel Application Programming

BL Massingill, TG Mattson, BA Sanders - Rapport technique, Universite de Floride, 1999 - cise.ufl.edu

... about the concurrency within the **intermediate** patterns ... Intervals provide an alternative **representation** of floating ... should be implemented using **thread**-safe shared ...

Cited by 6 - View as HTML - Web Search - cise.ufl.edu - cise.ufl.edu - cis.ufl.edu - all 5 versions »

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